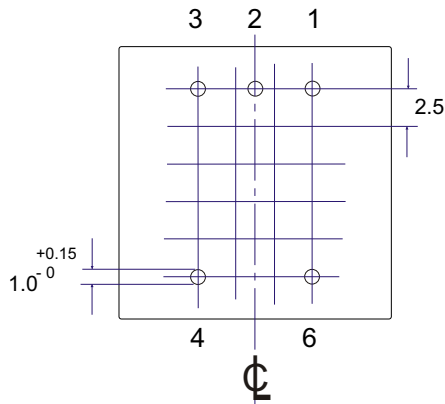
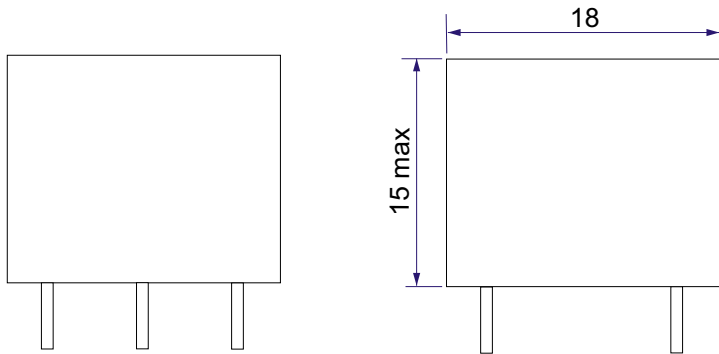
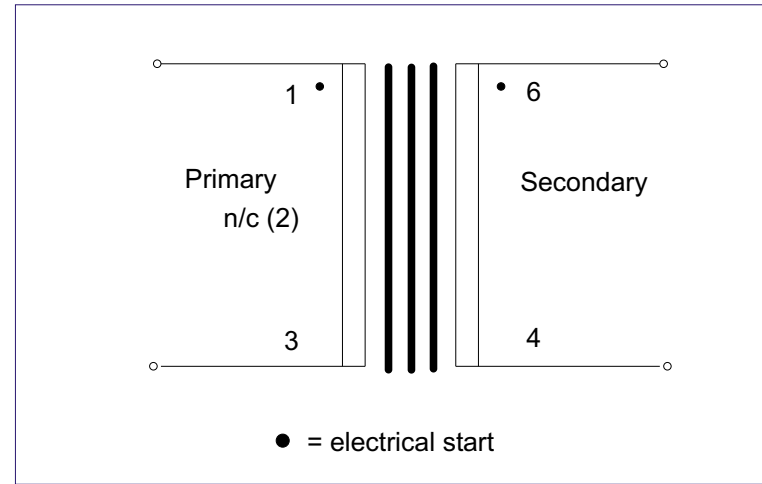




Label
YY WW = date code



Tolerance on all hole positions +/-0.5mm
Pin diameter = 0.6mm



Electrical specification:

Turns ratio: 1 to 1

DC resistance (+/-10%)

Primary: 54

Secondary: 75

Impedance matching: see page 3

Leakage inductance (@ 200mV): 2.7mH @100Hz typical
1.6mH @1kHz typical

Distortion: see page 21

Saturation: <10V rms @ 50Hz, 65V peak

Hi-pot, primary to secondary: 4kVAC, 2s, 0.5mA

Note: Do not pass DC current through windings.

Storage temperature range: -25°C to +125°C

Operating temperature range: 0°C to +85°C

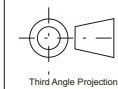


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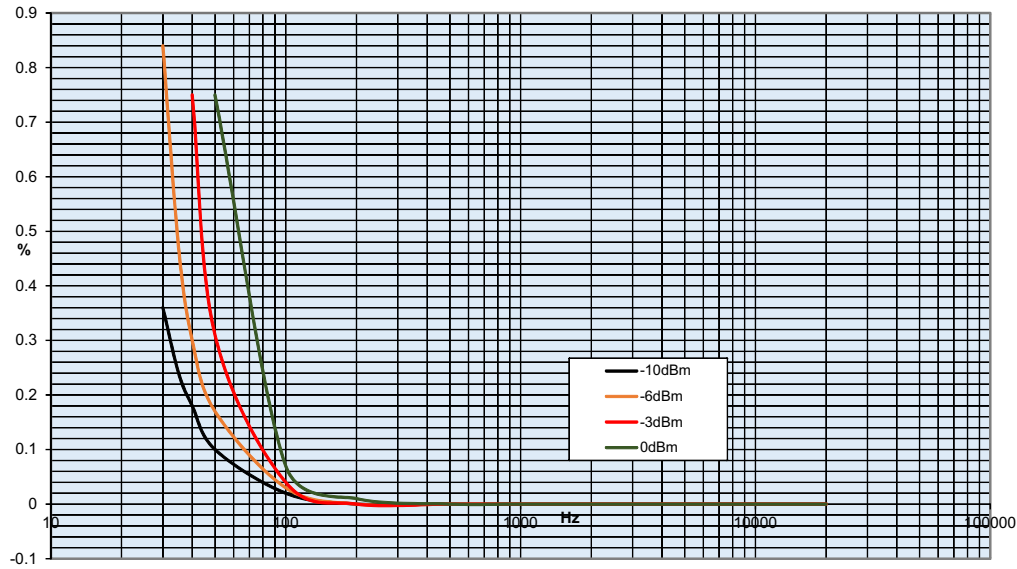
DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for TF048 page 1 of 3	1	08/11/99	CS		TF048
	5	19/08/13	CS		
	6	05/05/15	CS		
	7	07/03/19	CS		

Scale: 2 to 1

All dimensions in mm unless stated otherwise



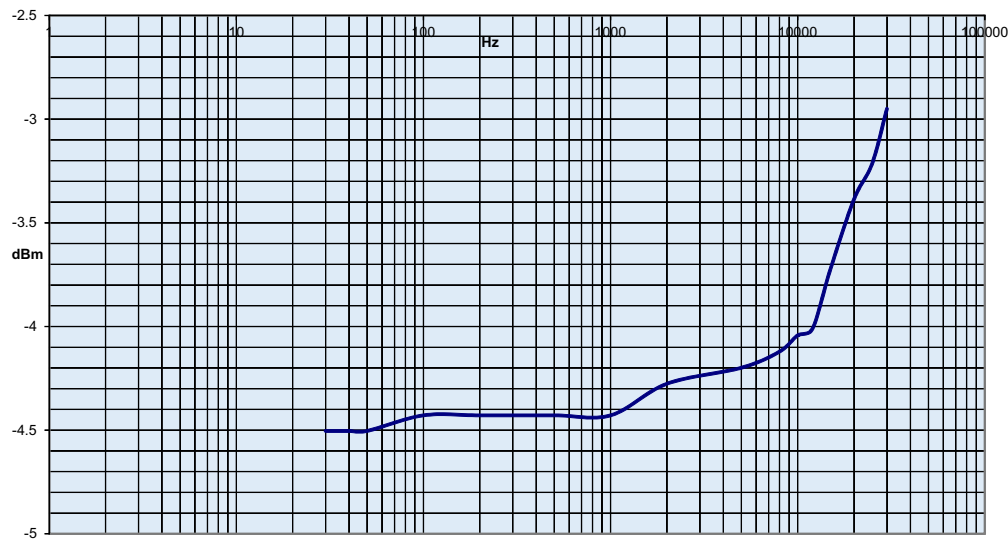
Distortion: secondary loaded with 477R



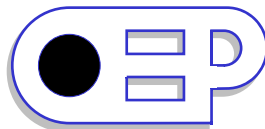
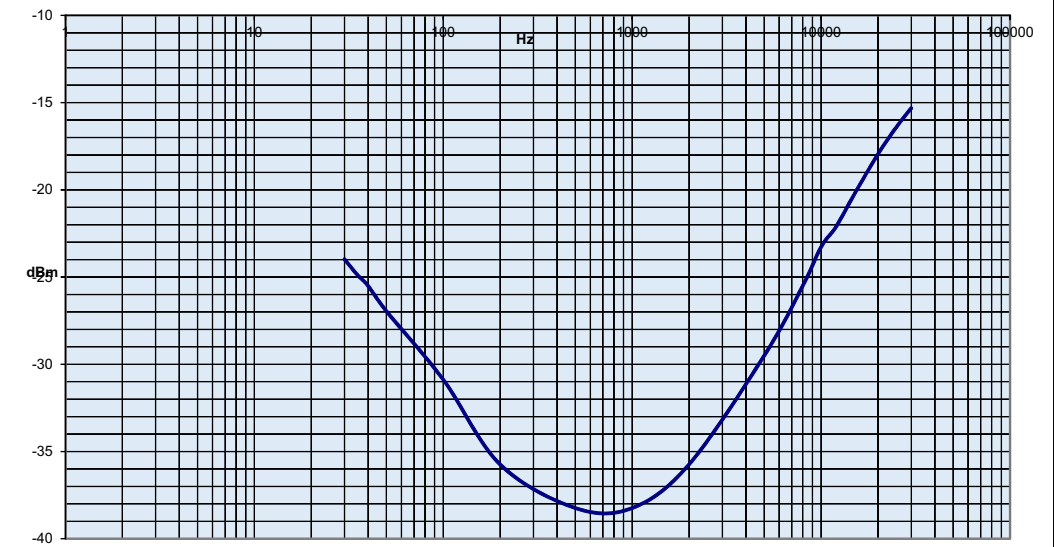
Frequency response, source: 0dBm, secondary loaded with 477R



Insertion Loss, source -10dBm, secondary loaded with 477R



Return loss, source -10dBm, secondary loaded with 477R

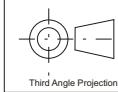


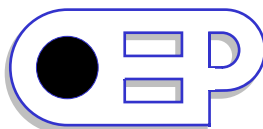
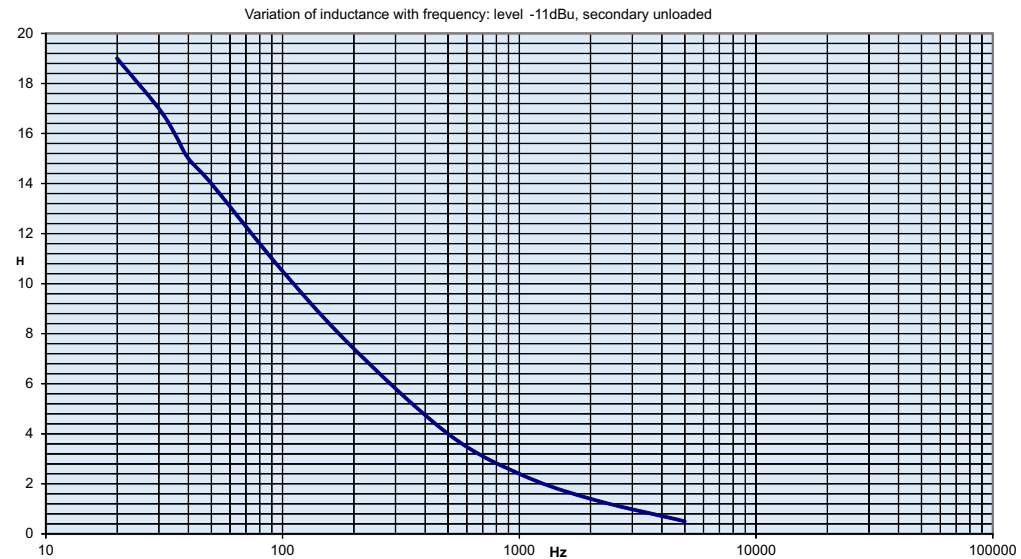
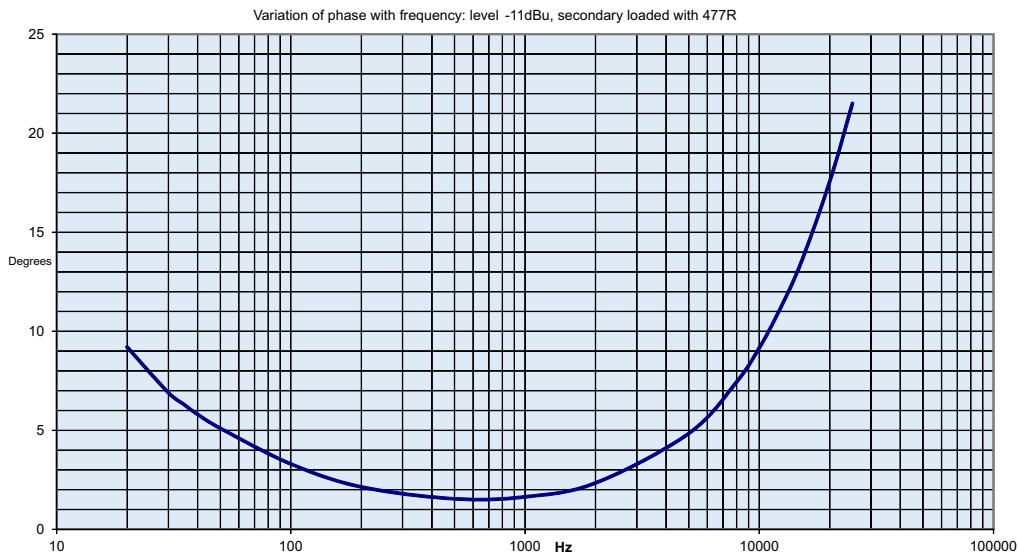
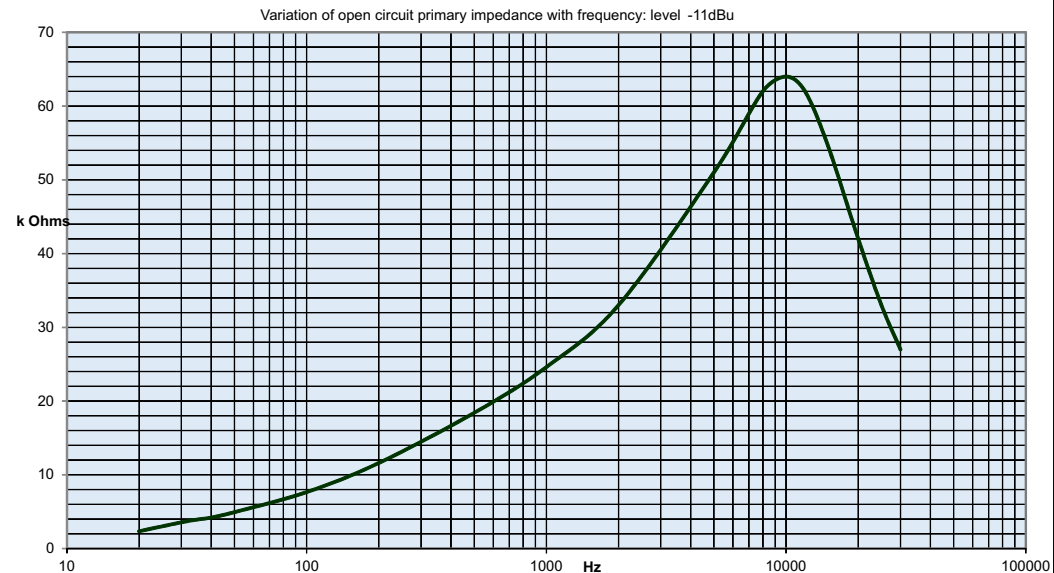
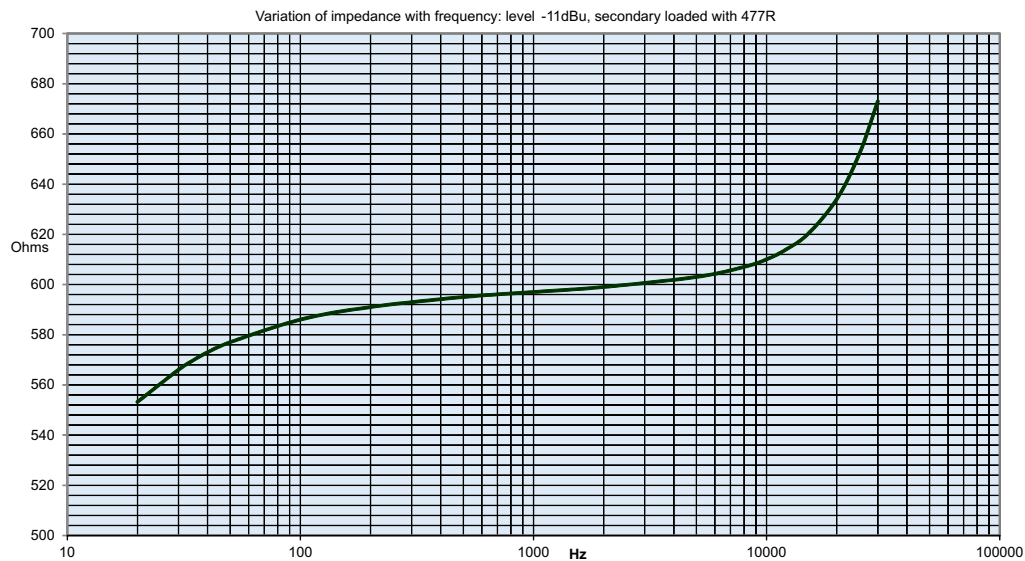
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Specification for TF048 page 2 of 3	1	08/11/99	CS		TF048
	5	19/08/13	CS		
	6	05/05/15	CS		
	7	07/03/19	CS		

Scale: N/A

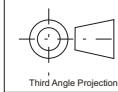
All dimensions in mm unless stated otherwise





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Specification for TF048 page 3 of 3	1	08/11/99	CS		TF048
	5	19/08/13	CS		
	6	05/05/15	CS		
	7	07/03/19	CS		



Scale: 2 to 1

All dimensions in mm unless stated otherwise