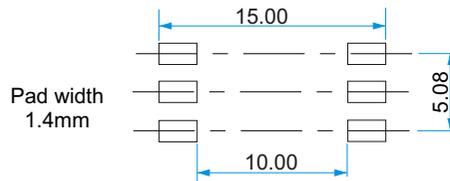
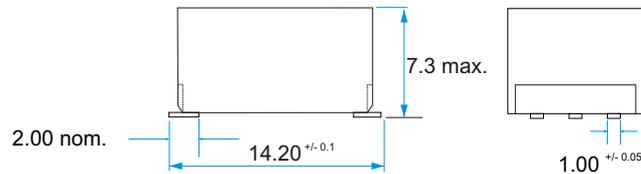
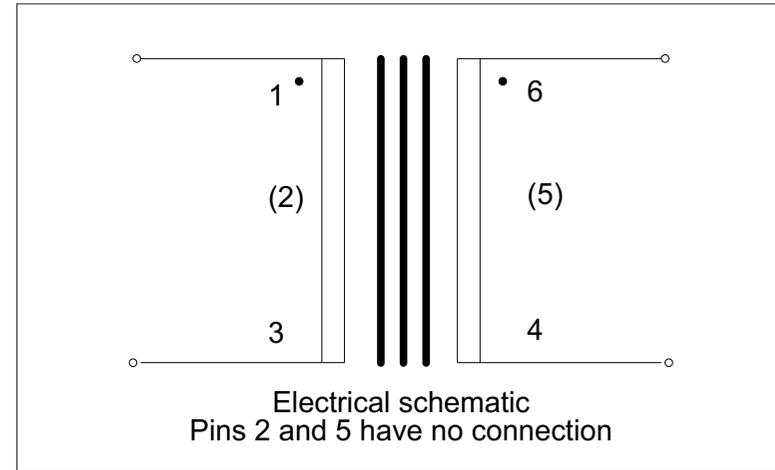


Part number
YYWW = date code



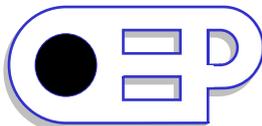
Suggested PCB layout



Electrical specification:

- Ratio: 1 to 1
- Primary DC resistance: 187 +/- 15%
- Secondary DC resistance: 187 +/- 15%
- Impedance matching: 600 to 600
- Inductance (250mVrms, 300Hz series) Pins 1 - 3: 2.6H min.
- Leakage inductance: (10mVrms, 1kHz series) pins 1 - 3: 19mH max.
- Return loss: (ref. 600 ohms) 200 to 4kHz: >22dB
- Insertion loss: (ref. 600 ohms, 200Hz - 4kHz): 3.0dB max.
- Frequency response: 200Hz - 4kHz: +/- 0.3dB
- Longitudinal balance: 200Hz - 4kHz: 80dB min.
- Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4: 1.00 +/- 1%
- Third harmonic distortion: 600Hz, 0dBm: -89dBm max.
- Saturation: <10Vrms, 65V peak, 50Hz
- Hi-pot, primary to secondary: 5500VDC min. 1mA for 2 seconds
- Operating temperature range: -20 to +85°C
- Storage temperature range: -40 to +125°C

Note: Do not pass DC current through windings.



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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for Q0807E	1	26/08/05	CS		Q0807E
	3	16/11/11	CS		
	4	31/10/12	CS		
	5	05/05/15	CS		
	Scale: 2 to 1	All dimensions in mm unless stated otherwise			