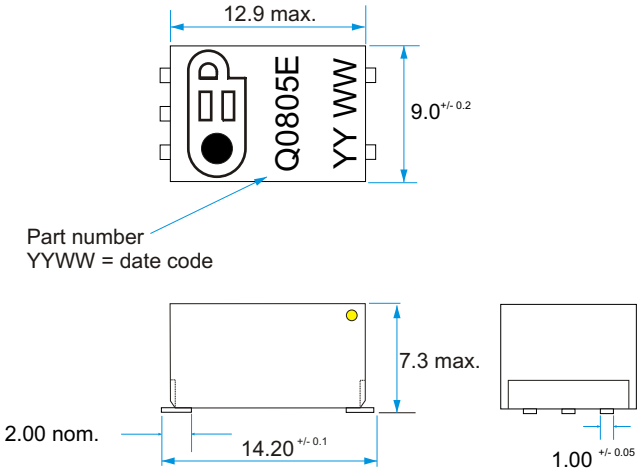
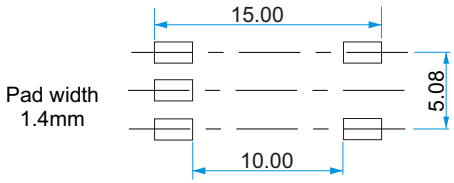
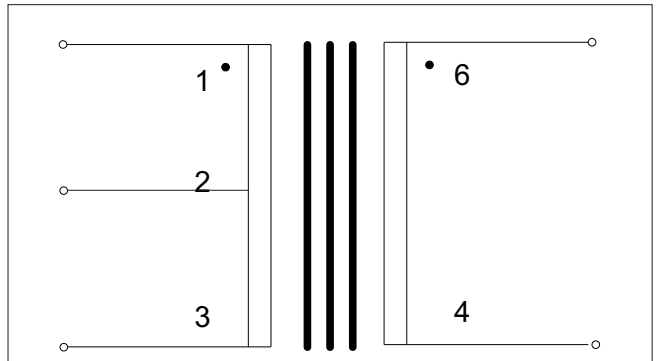




Suggested PCB layout



Electrical schematic

RoHS COMPLIANT

Electrical specification:

Ratio: 1CT to 1
Primary DC resistance: 53.0 + 58.4 ohms +/- 15%
Secondary DC resistance: 111.4 ohms +/- 15%
Impedance matching: 600 ohms to 600 ohms
Inductance (270mVrms, 100Hz parallel) Pins 1 - 3: 3.6H min.
Leakage inductance: (10mVrms, 200Hz series) pins 1 - 3: 4.1mH nom.
Return loss: (ref. 600 ohms) 200 to 4kHz: -18dB min.
Insertion loss: (ref. 600 ohms, 2kHz): 4dB max.
(ref. 430 ohms, 2kHz): 2dB max.
Frequency response: 200 - 4kHz: +/- 0.2dB
Longitudinal balance: 200Hz - 4kHz: 80dB min.
Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4:1.00+/- 1%
Distortion: 600Hz, 0dBm: -89dBm nom.
Saturation: <10Vrms, 65V peak, 50Hz
Hi-pot, primary to secondary: 3.3kV min., 1mA for 1 minute
Operating temperature range: -10 to +85°C
Storage temperature range: -40 to + +125°C

Note: Do not pass DC current through windings.

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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for Q0805E	1	30/04/01	CS		Q0805E
	2	01/08/11	CS		
	3	24/03/15	CS		

Scale: 2 to 1 All dimensions in mm unless stated otherwise