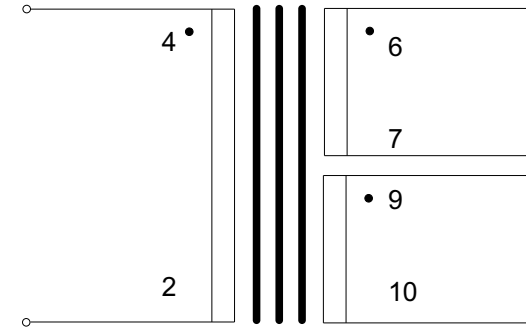


Pin view
Suggested PCB layout



Electrical schematic

Electrical specification:

Ratio: 2:1:1

DC resistance (+/-15%):

Primary (4 - 2): 1.48

Secondary 1 (6 - 7): 0.68

Secondary 2 (9 - 10): 0.83

Primary inductance: 10mH min. (@100kHz, 0.1V)

Primary leakage inductance: 8.8uH nom.

Interwinding capacitance: 25pF nom.

Isolation: winding to winding: 1.5kVrms for 2 seconds

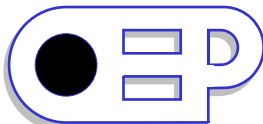
Creepage and clearance: 1.4mm min. (basic insulation)

Materials:

Bobbin: Nylon 46, 30% glass reinforced, e.g. Stanyl TE250 F6 UL file number E47960 class H or phenolformaldehyde glass-reinforced to UL94V-0, UL file number E41429(M) class H or equivalent.

Winding wire: grade 2 solderable class F minimum to IEC BS 60317-21e.g. Nexans Magnesol or equivalent.

Tape: Polyester film, e.g. 3M's No. 56 or No. 1350: thickness 0.06mm or Jingjiang Yahua type CT-280, UL file number E165111



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| DESCRIPTION | ISSUE | DATE | DRAWN | CHECKED | DRAWING NUMBER |
|-------------------------|-------|----------|-------|---------|----------------|
| Specification for PT8SM | 1 | 15/08/07 | CS | | PT8SM |
| | 4 | 16/07/08 | CS | | |
| | 5 | 07/06/10 | CS | | |
| | 6 | 23/02/15 | CS | | |

Scale: 2 to 1

All dimensions in mm unless stated otherwise