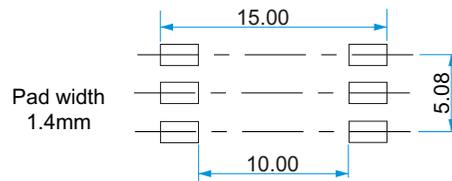
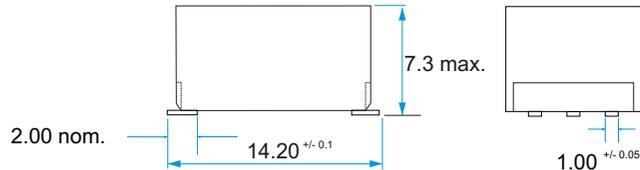
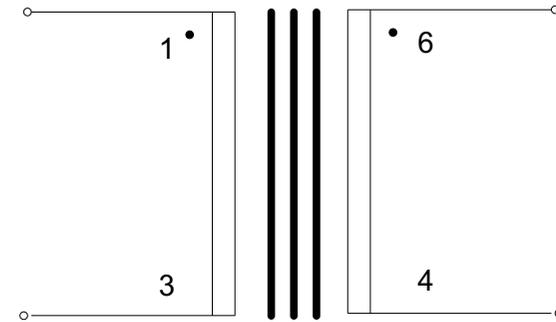


Part number  
YYWW = date code



Suggested PCB layout



Electrical schematic

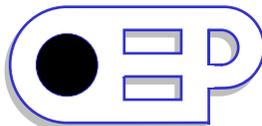
**Electrical specification:**

Ratio: 1 to 1  
 DC resistance (sum of windings): 191 - 258  
 Impedance matching: 600 ohms to 600 ohms  
 Inductance (270mVrms, 100Hz parallel) Pins 1 - 3: 3.6H min.  
 Leakage inductance: (10mVrms, 200Hz series) pins 1 - 3: 4.1mH nom.  
 Return loss: (ref. 600 ohms) 200 to 4kHz: -18dB min.  
 Insertion loss: (ref. 600 ohms, 2kHz): 2dB max.  
 (ref. 430 ohms, 2kHz): 4dB max.  
 Frequency response: 200 - 4kHz: +/- 0.2dB  
 Longitudinal balance: 200Hz - 4kHz: 80dB min.  
 Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4: 1.00 +/- 1%  
 Distortion: 600Hz, 0dBm: -100dBm nom.  
 Saturation: <10Vrms, 65V peak, 50Hz  
 Hi-pot, primary to secondary: 5500VDC, 1mA for 2 seconds  
 Operating temperature range: -20 to +85°C  
 Storage temperature range: -40 to +125°C

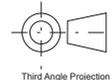
Certified to EN60950-1: 2001

RoHS process compliant.

**Note: Do not pass DC current through windings.**



Unit 5, Oxonian Park, Langford Locks,  
 Kidlington, Oxfordshire. OX5 1FP  
 Tel: (01865) 855085 Fax: (01865) 855075  
 Website: [www.oep.co.uk](http://www.oep.co.uk)

DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for OEP8034 	1	21/06/05	CS		<b>OEP8034</b>
	6	13/01/10	CS		
	7	10/05/10	CS		
	8	23/02/15	CS		

Scale: nts

All dimensions in mm unless stated otherwise